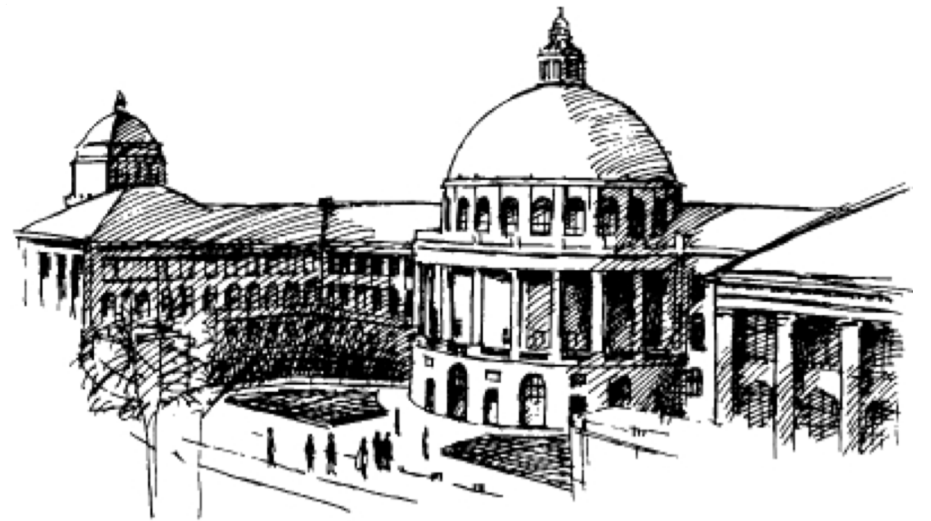
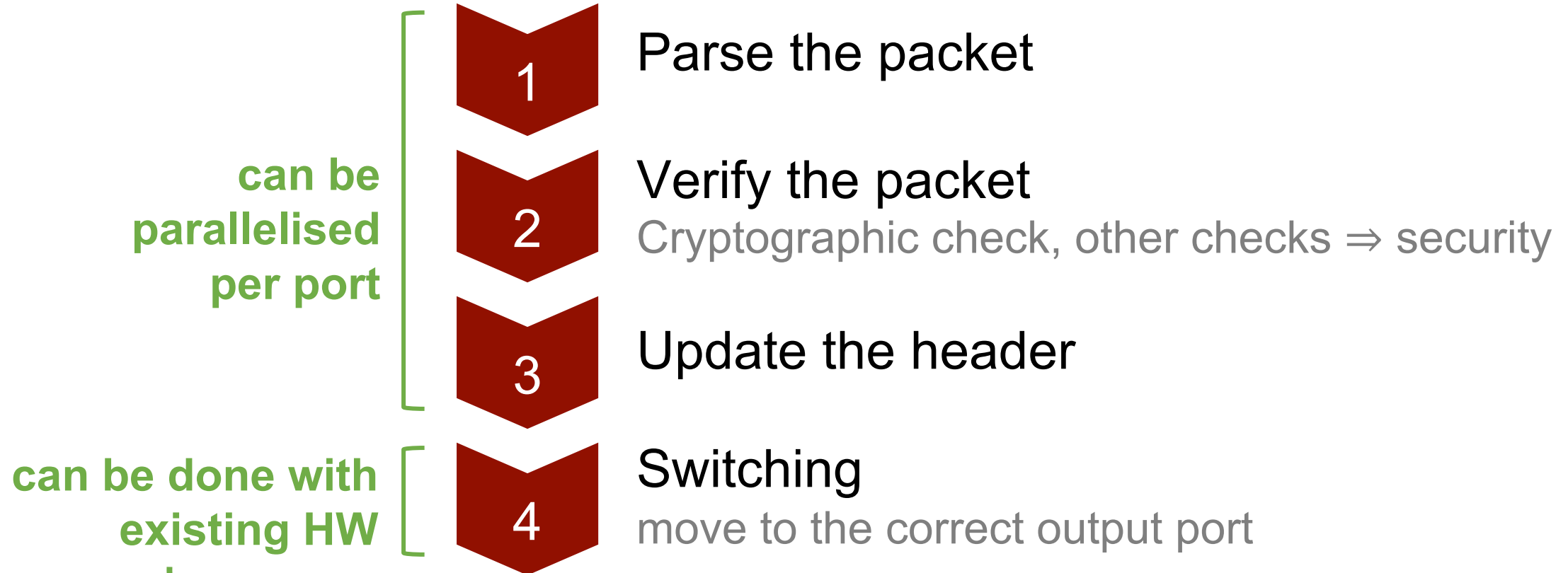


SCION at Terabit Speeds

Presented by Kamila Součková

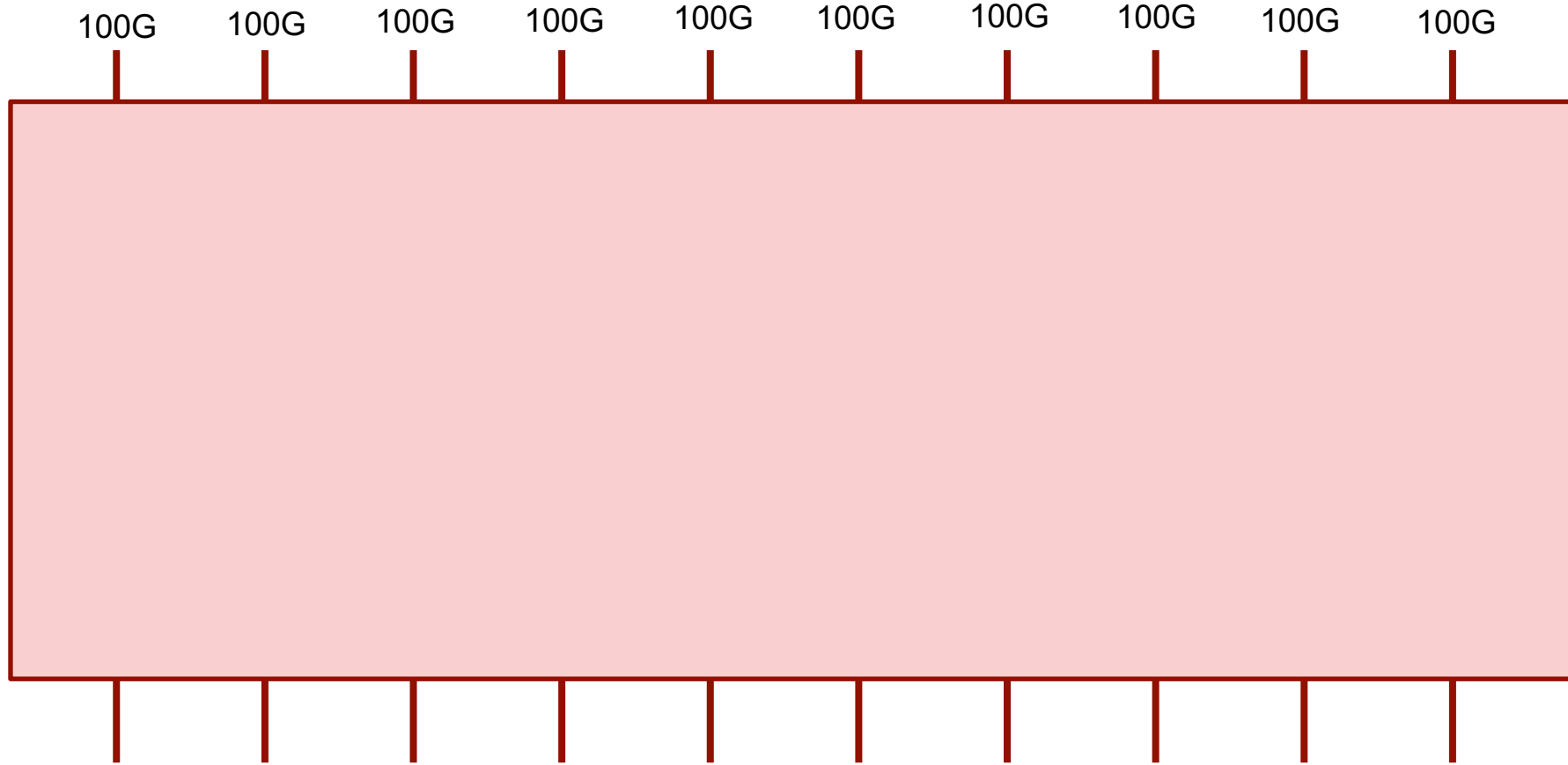


Life of a SCION packet

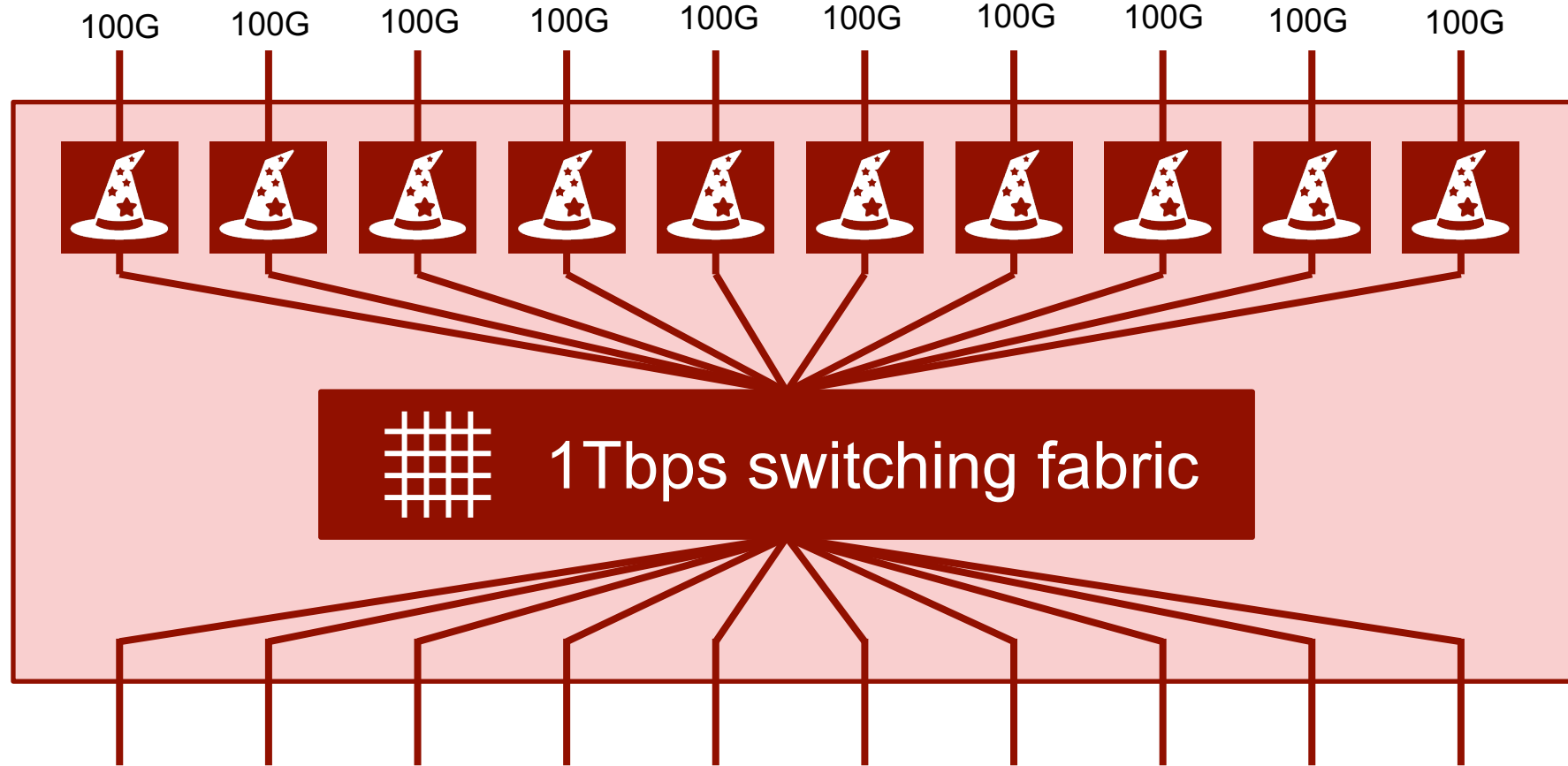


32x100GbE, ~8000 CHF

Terabit router design



Terabit router design

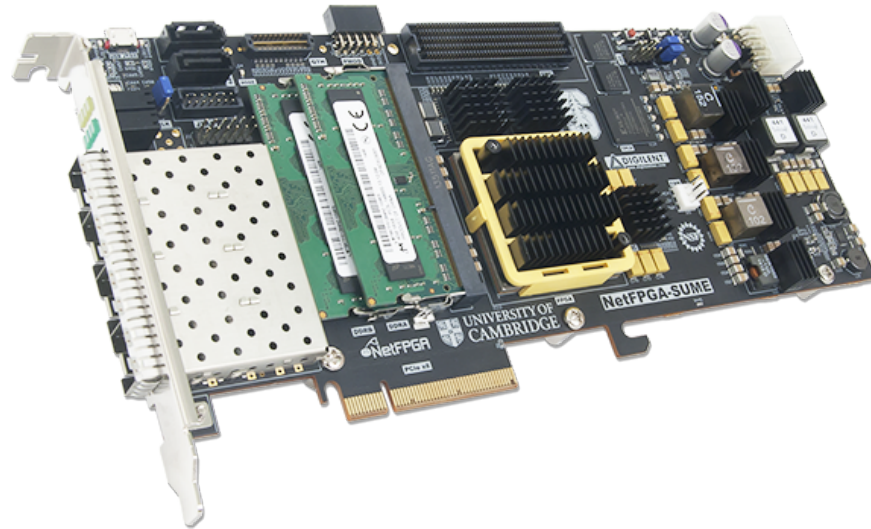


Terabit router design



Terabit router design

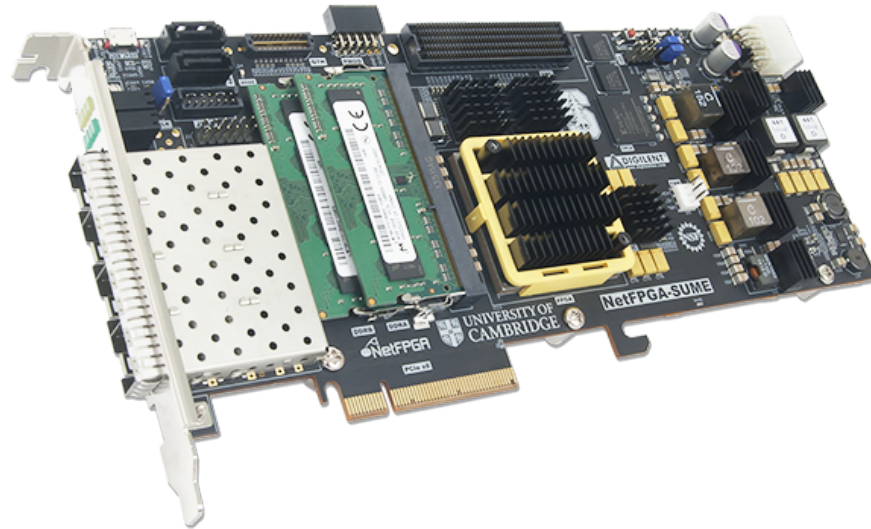
FPGA



NetFPGA SUME:
4x 10GbE, Virtex 7 FPGA

Terabit router design

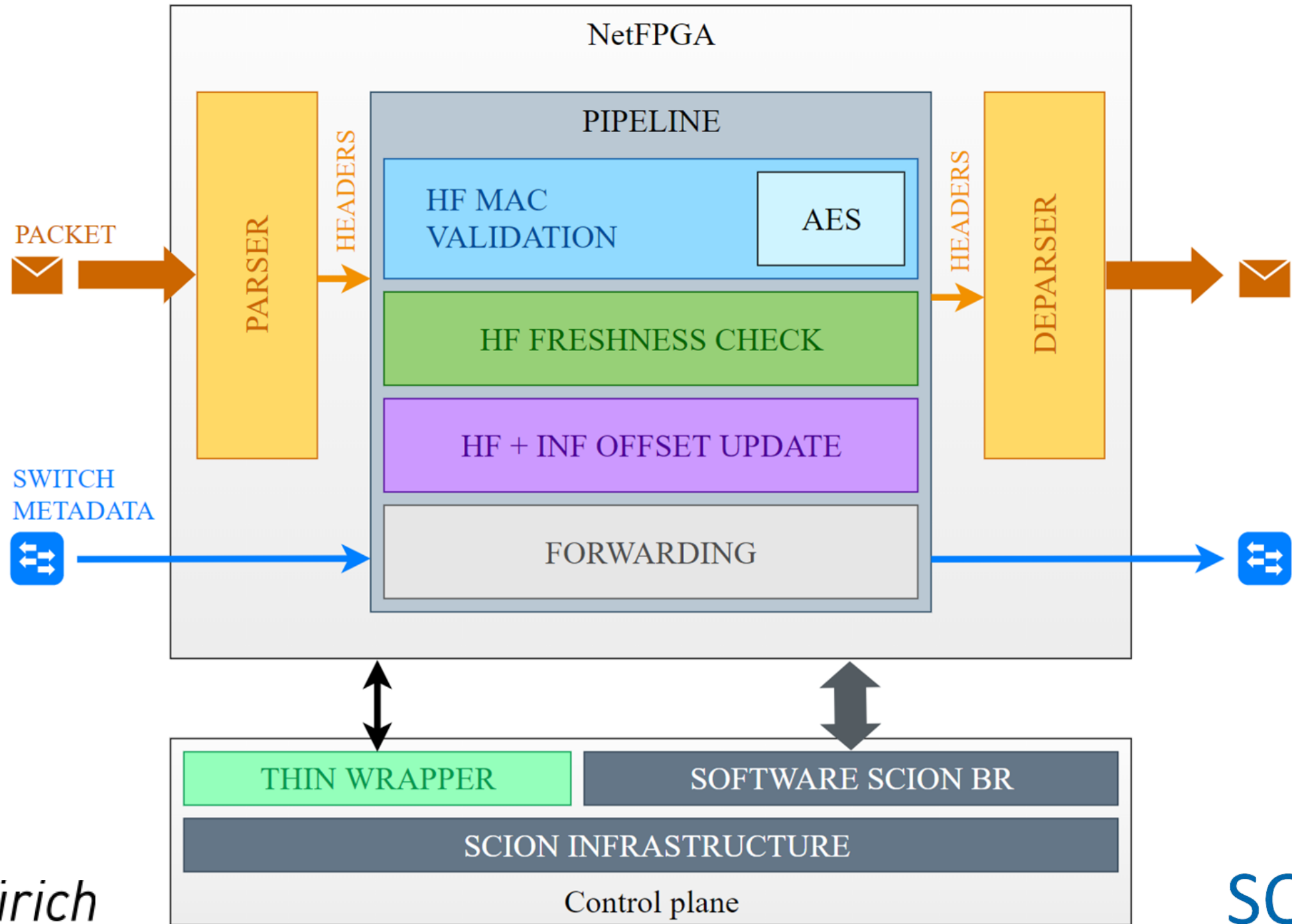
FPGA



NetFPGA SUME:
4x 10GbE, Virtex 7 FPGA



also a
self-contained
router



Port Traffic and Counters > Basic Traffic Results

Change Result View

1 of 1

Basic Counters | Errors | Triggers | Protocols

Port Name	Total Tx Count (Frames)	Total Rx Count (Frames)
Port //1/1	0	0
Port //1/2	0	0
Port //1/3	0	0
Port //1/4	0	0

Σ 0 0

scion chart

Color Counter Y Axis?

Port //1/1.Rx...	Left 1
Port //1/1.Tx...	Left 1
Port //1/2.Rx...	Left 1
Port //1/2.Tx...	Left 1
Port //1/3.Rx...	Left 1
Port //1/3.Tx...	Left 1
Port //1/4.Rx...	Left 1
Port //1/4.Tx...	Left 1

Port Traffic and Counters > Aggregate Port L1 R

Change Result View

Aggregate Port L1 Rx Rate

000.00